Amendment to the Claims

This listing of the claims will replace all prior versions and listings, of the claims in the application:

Claim 1 (previously presented): An optical inspection system for inspecting an object, comprising:

a plurality of cameras for imaging the object, each of the plurality of cameras being asynchronously triggerable;

an illumination system for providing a plurality of lighting modes to illuminate the object for the plurality of cameras;

a frame grabber unit for transmitting image data from the plurality of cameras to a memory; and

a main computer for controlling image acquisition of the object,

wherein the plurality of cameras are adapted to obtain image data of the object based upon a plurality of fields of view of the object and a series of firing positions within each field of view, each of the firing positions having associated therewith at least one of the plurality of cameras and at least one of the plurality of lighting modes provided by the illumination system, wherein the optical image system is adapted to image a first one of the plurality of fields of view of the object with the at least one of the plurality of cameras in first and second ones of the plurality of lighting modes in a single pass.

Claim 2 (canceled)

Claim 3 (original): The system according to claim 1, wherein image data provided by the plurality of cameras is sent to the memory concurrently.

Claim 4 (original): The system according to claim 3, wherein the memory is main memory that is directly accessible by the main computer.

MacPherson Kwok Chen & Heid LLP
1762 Technology Drive,
Suite 226
San Jose, CA 95110
Telephone: (408) 392-9250
Facsimile: (408) 392-9262

Claim 5 (original): The system according to claim 1, wherein the plurality of cameras includes at least four cameras.

Claim 6 (original): The system according to claim 1, wherein the object is a printed circuit board.

Claim 7 (original): The system according to claim 1, wherein the frame grabber unit includes a plurality of DMA channels for storing data in the memory, which is directly accessible by the main computer.

Claim 8 (original): The system according to claim 1, wherein the frame grabber unit includes at least one image acquisition board having a plurality of DMA channels for transmitting image data from at least two of the plurality of cameras to the memory, which is directly accessible by the main computer.

Claim 9 (original): The system according to claim 1, further including a movable head assembly to which the plurality of cameras are secured and a position encoder for providing position information of the head assembly.

Claim 10 (previously presented): The system according to claim 9, wherein a velocity of the head assembly is adjustable to minimize inspection time of the object.

Claim 11 (previously presented): The system according to claim 1, further including an event memory for storing firing position data, camera trigger data, and a lighting mode for each of the plurality of firing positions.

MacPherson Kwok Chen & Heid LLP
1762 Technology Drive,
Suite 226
San Jose, CA 95110
Telephone: (408) 392-9250
Facsimile: (408) 392-9262

Claim 12 (currently amended): An optical inspection system for inspecting an object, comprising:

a plurality of triggerable cameras for imaging the object;

an illumination system for providing a plurality of lighting modes to illuminate the object for the plurality of cameras;

a main computer coupled to the plurality of cameras and the illumination system; and

a frame grabber unit for receiving image data from the plurality of cameras, wherein the frame grabber unit includes at least one image acquisition board having a plurality of channels for transmitting image data from at least two of the plurality of cameras concurrently to main memory, which is directly accessible by the main computer, wherein each of the plurality of channels corresponds to a DMA channel.

Claim 13 (previously presented): The system according to claim 12, wherein the optical inspection system is adapted to image a first location on the object with a first one of the plurality of cameras in first and second ones of the plurality of lighting modes in a single pass over the first location.

Claim 14 (canceled)

Claim 15 (previously presented): The system according to claim 12, wherein main memory is adapted to store image data for more than one stripe.

Claim 16 (original): The system according to claim 12, wherein the object is a circuit board.

Claim 17 (previously presented): A method of inspecting a circuit board, comprising:

selecting a speed for movement of a head assembly supporting a plurality of cameras with respect to the circuit board;

dividing the circuit board into field of views arranged in stripes, each field of view including a plurality of firing positions;

for each of the plurality of firing positions, selecting at least one of a plurality of asynchronously triggerable cameras and at least one of a plurality of lighting modes, wherein the optical inspection system is adapted to image a first location on the circuit board with the at least one of the plurality of cameras in first and second ones of the plurality of lighting

MacPherson Kwok Chen & Heid LLP
1762 Technology Drive,
Suite 226
San Jose, CA 95110
Telephone: (408) 392-9250
Facsimile: (408) 392-9262

modes in a single pass; and

transmitting image data from the plurality of cameras to memory.

Claim 18 (original): The method according to claim 17, further including minimizing an inspection time of the circuit board from the head assembly speed, a number of lighting modes, and a number of passes over the circuit board required to image the circuit board.

Claim 19 (original): The method according to claim 17, further including transmitting the image data to main memory that is directly accessible to a processor for analyzing the image data.

Claim 20 (original): The method according to claim 19, further including transmitting the image data over a plurality of DMA channels.

Claim 21 (original): The method according to claim 20, further including transmitting the image data from a plurality of cameras concurrently.

Claim 22 (previously presented): The method according to claim 17, further including imaging the circuit board in one pass over each stripe of the board, wherein at least one location on the board is imaged in at least two different lighting modes.

Claim 23 (currently amended): A method of manufacturing a circuit board, comprising:

fabricating a printed circuit board;

populating the circuit board with components;

soldering the components to the circuit board to provide a circuit board assembly; and

inspecting the circuit board assembly by

selecting a speed for movement of a head assembly supporting a plurality of cameras with respect to the circuit board,

dividing the circuit board into a plurality of field of views, each of which includes a plurality of firing positions;

MacPherson Kwok Chen & Heid LLP
1762 Technology Drive,
Suite 226
San Jose, CA 95110
Telephone: (408) 392-9250
Facsimile: (408) 392-9262

for each of the plurality of firing positions, selecting at least one of a plurality of asynchronously triggerable cameras and at least one of a plurality of lighting modes, wherein the method of manufacturing is adapted to image a first location on the circuit board with the at least one of the plurality of cameras in first and second ones of the plurality of lighting modes in a single pass, and

transmitting image data from the plurality of cameras to memory.

MacPherson Kwok Chen & Heid LLP
1762 Technology Drive,
Suite 226
San Jose, CA 95110
Telephone: (408) 392-9250
Facsimile: (408) 392-9262